TSOP953.., TSOP955..

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



- Improved dark sensitivity
- Improved immunity against optical noise
- Very low supply current
- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.0 V to 3.6 V
- Insensitive to supply voltage ripple and noise
- Material categorization: definitions of compliance please for see www.vishay.com/doc?99912

MECHANICAL DATA

1, 4 = GND, 2 = V_S, 3 = OUT

ORDERING CODE

Taping: TSOP95...TT - top view taped TSOP95...TR - side view taped

DESCRIPTION

The TSOP95... series devices are the latest generation miniaturized IR receiver modules for infrared remote control systems. These series provide improvements in sensitivity to remote control signals in dark ambient as well as in sensitivity in the presence of optical disturbances e.g. from CFLs.

The devices contain a PIN diode and a preamplifier assembled on a lead frame. The epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP953.. and TSOP955.., series devices are designed to receive short burst codes (6 or more carrier cycles per burst). The third digit designates the AGC level (AGC3 or AGC5) and the last two digits designate the band-pass frequency (see table below). The higher the AGC, the better noise is suppressed, but the lower the code compatibility. AGC3 provides enhanced noise suppression and AGC5 provides maximized noise suppression. Generally, we advise to select the highest AGC that satisfactorily receives the desired remote code.

These components have not been qualified to automotive specifications.

PARTS TABLE				
AGC		ENHANCED NOISE SUPPRESSION (AGC3)	MAXIMIZED NOISE SUPPRESSION (AGC5)	
	30 kHz	TSOP95330	TSOP95530	
Carrier frequency	33 kHz	TSOP95333	TSOP95533	
	36 kHz	TSOP95336 ⁽¹⁾	TSOP95536	
	38 kHz	TSOP95338 ⁽²⁾⁽⁴⁾	TSOP95538	
	40 kHz	TSOP95340	TSOP95540	
	56 kHz	TSOP95356	TSOP95556 ⁽³⁾	
Package		Heimdall		
Pinning		1, 4 = GND, 2 = V _S , 3 = OUT		
Dimensions (mm)		6.8 W x 3.0 H x 3.2 D		
Mounting		SMD		
Application		Remote control		
Best choice for		⁽¹⁾ RCMM ⁽²⁾ RECS-80 Code ⁽³⁾ r-map ⁽⁴⁾ XMP-1, XMP-2		

Note

30 kHz and 33 kHz only available on written request

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RoHS COMPLIANT

HALOGEN

FREE GREEN

(5-2008)



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BLOCK DIAGRAM



APPLICATION CIRCUIT



 $R_{\rm 1}$ and $C_{\rm 1}$ recommended to reduce supply ripple for $V_{\rm S}$ < 2.2 V

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		Vs	-0.3 to +3.6	V
Supply current		I _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Quark contact	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.25	0.37	0.45	mA
Supply current	E _v = 40 klx, sunlight	I _{SH}	-	0.50	-	mA
Supply voltage		Vs	2.0	-	3.6	V
Transmission distance	$E_v = 0$, test signal see Fig. 1, IR diode TSAL6200, I _F = 50 mA	d	-	24	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	Test signal: XMP code	E _{e min.}	-	0.16	0.35	mW/m ²
	Test signal: NEC code	E _{e min.}	-	0.12	0.30	mW/m ²
$\label{eq:maximum irradiance} \begin{array}{c} t_{pi} - 3.0/f_0 < t_{po} < t_{pi} + 3.5/f_0, \\ test \ signal \ see \ Fig. \ 1 \end{array}$		E _{e max.}	30	-	-	W/m ²
Directivity	Angle of half transmission distance	φ1/2	-	± 50	-	o

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Fig. 3 - Test Signal



Fig. 4 - Pulse-Width vs. Irradiance in Dark Ambient



Fig. 5 - Frequency Dependence of Responsivity



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Fig. 7 - Sensitivity vs. Supply Voltage Disturbances



Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length



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Fig. 10 - Relative Spectral Sensitivity vs. Wavelength





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Fig. 12 - Sensitivity vs. Supply Voltage



SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

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Fig. 13 - IR Emission from Fluorescent Lamp With Low Modulation



Fig. 14 - IR Emission from Fluorescent Lamp With High Modulation

	TSOP953	TSOP955
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length A gap time is required of	6 to 20 cycles ≥ 8 cycles	6 to 38 cycles ≥ 8 cycles
For bursts greater than a minimum gap time in the data stream is needed of	20 cycles > 6 x burst length	38 cycles > 20 ms
Maximum number of continuous short bursts/second	2500	2500
RCMM code	Preferred	Yes
XMP-1 code	Preferred	Yes
r-map code	Yes	Preferred
RECS-80 code	Preferred	Yes
Suppression of interference from fluorescent lamps	Fig. 13 and Fig. 14	Fig. 13 and Fig. 14

Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP952.., TSOP954.., or TSOP956..



PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off

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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TR DIMENSIONS in millimeters





Drawing-No.: 9.700-5337.01-4 Issue: 2; 06.10.15 technical drawings according to DIN specifications

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TAPING VERSION TSOP..TT DIMENSIONS in millimeters





technical drawings according to DIN specifications

Drawing-No.: 9.700-5338.01-4 Issue: 4; 12.06.13



REEL DIMENSIONS in millimeters





COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 ± 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

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VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)			
PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	xxxxxx+	Company logo	
Long bar code top	Туре	Length	
Item-number	Ν	8	
Plant-code	Ν	2	
Sequence-number	Х	3	
Quantity	Ν	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	Х	3	
Data-code	N	3	
Batch-number	Х	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard J-STD-020 level 4 label is included on all dry bags.

\oslash	CAUTION This bag contai MOISTURE-SENSITIVE		
 Shelf life i humidity (in sealed bag: 12 months at < 40 (RH)	°C and < 90 % relative	
 After this bag is opened, devices that will be subjected to soldering reflow or equivalent processing (peak package body termp. 260 °C) must be Abounted within 72 hours at factory condition of < 30 °C/60 % RH or 2b. Stored at <5 % RH 			
Humidity	equire baking befor mounting if: Indicator Card is > 10 % when re are not met.	ad at 23 °C ± 5 °C or	
4. If baking is required, devices may be baked for: 192 hours at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or 96 hours at 60 °C ± 5 °C and < 5 % RH for all device containers or 24 hours at 125 °C ± 5 °C not suitable for reels or tubes			
Bag Seal Date		de John N	
(If blank, see barcode label) Note: Level and body temperature defined by EIA JEDEC Standard J-STD-02			

EIA JEDEC standard J-STD-020 level 4 label is included

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on all dry bags

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging. Vishay Semiconductors

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

PartNo: TSOP75236TT 01Y: 2200 Selfcode:100 Orgin PHIL PPINES Machine: T2 >TC: 19	Lot: F0033958.0 Lot: 1910/2532 A VISHAY Region: 2110 St. 0010 Sentat: WMASO(55)(6) Operator 327 C k-0 F96 Reits (h-fm)

22178



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